# **PNP Silicon Epitaxial Planar Transistor**

Low frequency transistor



1.Base 2.Collector 3.Emitter SOT-89 Plastic Package

### Absolute Maximum Ratings (T<sub>a</sub> = 25 °C)

Parameter	Symbol	Value	Unit	
Collector Base Voltage	-V <sub>CBO</sub>	30	V	
Collector Emitter Voltage	-V <sub>CEO</sub>	20	V	
Emitter Base Voltage	-V <sub>EBO</sub>	6	V	
Collector Current - DC Collector Current - Pulse 1)	-I <sub>C</sub> -I <sub>CP</sub>	5 10	Α	
Collector Power Dissipation	P <sub>C</sub>	0.5 2	W	
Junction Temperature	T <sub>J</sub>	150	°C	
Storage Temperature Range	T <sub>S</sub>	- 55 to + 150	°C	

<sup>&</sup>lt;sup>1)</sup> Single pulse,  $P_W = 10 \text{ ms.}$ 

#### Characteristics at T<sub>a</sub> = 25 °C

Parameter		Symbol	Min.	Тур.	Max.	Unit
DC Current Gain at -V <sub>CE</sub> = 2 V, -I <sub>C</sub> = 500 mA Current Gain Grou	ıp P Q R	h <sub>FE</sub> h <sub>FE</sub>	82 120 180	- - -	180 270 390	- - -
Collector Base Cutoff Current at -V <sub>CB</sub> = 20 V		-I <sub>CBO</sub>	-	-	0.5	μA
Emitter Base Cutoff Current at -V <sub>EB</sub> = 5 V		-I <sub>EBO</sub>	-	-	0.5	μA
Collector Base Breakdown Voltage at $-I_C = 50 \mu A$		-V <sub>(BR)CBO</sub>	30	-	-	V
Collector Emitter Breakdown Voltage at -I <sub>C</sub> = 1 mA		-V <sub>(BR)CEO</sub>	20	-	-	V
Emitter Base Breakdown Voltage at $-I_E = 50 \mu A$		-V <sub>(BR)EBO</sub>	6	-	-	V
Collector Emitter Saturation Voltage at $-I_C = 4 \text{ A}$ , $-I_B = 100 \text{ mA}$		-V <sub>CE(sat)</sub>	ı	-	1	V
Transition Frequency at $-V_{CE} = 6 \text{ V}$ , $I_E = 50 \text{ mA}$ , $f = 100 \text{ MHz}$		f <sub>T</sub>	-	120	-	MHz
Output Capacitance at $-V_{CB} = 20 \text{ V}$ , $I_E = 0$ , $f = 1 \text{ MHz}$		C <sub>ob</sub>	-	60	-	pF









<sup>&</sup>lt;sup>2)</sup> When mounted on a 40 X 40 X 0.7 mm ceramic board.

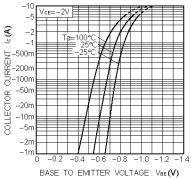


Fig.1 Grounded emitter propagation characteristics

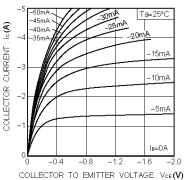


Fig.2 Grounded emitter output characteristics

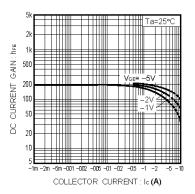


Fig.3 DC current gain vs. collector current (I)

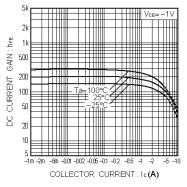


Fig.4 DC current gain vs. collector current (II)

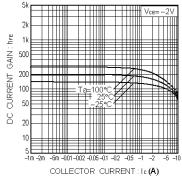


Fig.5 DC current gain vs. collector current (III)

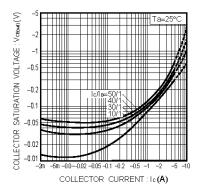


Fig.6 Collector-emitter saturation voltage vs. collector current (I)

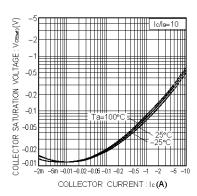
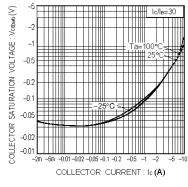


Fig.7 Collector-emitter saturation voltage vs. collector current (II)



Collector-emitter saturation voltage vs. collector current (III)

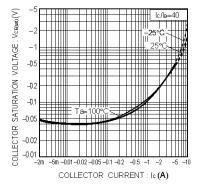


Fig.9 Collector-emitter saturation voltage vs. collector current (IV)



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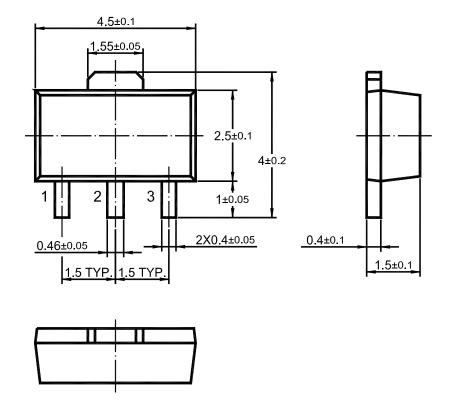








#### **SOT-89 PACKAGE OUTLINE**



Dimensions in mm







